

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Applicants : Karlheinz Freywald  
Serial No. : 10/588,415  
Filing Date : 07/14/2008  
Title : FILLING OF INSULATION TRENCHES USING CMOS-  
STANDARD PROCESSES FOR CREATING  
DIELECTRICALLY INSULATED AREAS ON A SOI WAFER  
Docket No. : LEO 009 PA  
Examiner : Thinh T. Nguyen  
Art Unit : 2818  
Confirmation No. : 8357

Commissioner for Patents  
Alexandria, VA 22313-1450

RESPONSE TO COMMUNICATION  
ELECTION/RESTRICTION

Sir:

In response to the Office action of 05/12/2010, please see the  
**Remarks/Arguments** that begin on page 2 of the paper.